

8. The display panel of claim 7, further comprising a common electron injection layer (EIL) underneath the common ETL, and over the first reflective electrode and the second reflective electrode.

9. The display panel of claim 4, further comprising a first ETL underneath the first emission layer and over the first reflective electrode, and a second ETL underneath the second emission layer and over the second reflective electrode.

10. The display panel of claim 9, wherein a first distance from a top surface of the first reflective electrode to a bottom surface of the first emission layer, and a second distance from a top surface of the second reflective electrode to a bottom surface of the second emission layer are both less than 50 nm.

11. The display panel of claim 4, wherein a first distance from a top surface of the first reflective electrode to a bottom surface of the first emission layer, and a second distance from a top surface of the second reflective electrode to a bottom surface of the second emission layer are both less than 50 nm.

12. The display panel of claim 4, further comprising a common hole injection layer (HIL) over the first HTL and the second HTL.

13. The display panel of claim 4, wherein the top electrode layer comprises a transparent conductive oxide (TCO) layer.

14. The display panel of claim 4, wherein the top electrode layer comprises a layer stack.

15. The display panel of claim 14, wherein the layer stack includes a metal layer and a TCO layer.

16. The display panel of claim 4, wherein a first distance from a top surface of the first reflective electrode to a bottom surface of the first emission layer is at least an order of magnitude less than a primary peak of the first narrow band emission wavelength range.

17. The display panel of claim 16, wherein the primary peak of the first narrow band emission wavelength range is between 620 nm and 750 nm.

18. The display panel of claim 17, wherein the first distance is less than 50 nm.

19. The display panel of claim 16, wherein a second distance from a top surface of the second reflective electrode to a bottom surface of the second emission layer is at least an order of magnitude less than a primary peak of the second narrow band emission wavelength range.

20. The display panel of claim 4, further comprising a third subpixel comprising:

a third reflective electrode;

a third emission layer over the third reflective electrode, the third emission layer designed for a third narrow band emission wavelength range that is different from the first narrow band emission wavelength range and the second narrow band emission wavelength range; and

a third HTL characterized by a third thickness over the third emission layer, wherein the third thickness is different from the first thickness and the second thickness;

wherein the semi-transparent or transparent top electrode layer is over the first, second, and third hole transport layers; and

wherein the third narrow band emission wavelength range is 35 nm or less full-width-at-half-maximum, the third emission layer comprises quantum dots of a third composition, the second reflective electrode is more reflective to the second narrow band emission wavelength range than the top electrode layer, and the third reflective electrode is more reflective to the third narrow band emission wavelength range than the top electrode layer.

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